Standardized Software Architecture  
(Preliminary Set of Requirements & Objectives)

- Pervasive software architectures
- Scalable and reconfigurable architectures
- Consistent with SI hardware specifications (SIWG)
- Supports multiple processing platforms (CPU, FPGA, GPU, etc.)
- Supports multiple standards (VXI, PXI, LXI, AXI, PCIe, Ethernet, etc.)
- Addresses User Interfaces (UI, GUI, etc.)
- Addresses TPS portability, reuse, and TPS development
- Supports software reuse (organic, 3rd Party, etc.)
- Addresses end-user application development (organic)
- Addresses concurrent operation of instruments
- Allows mechanisms for protecting Intellectual Property (IP)
- Supports hardware “swapability”
- Supports multiple software development environments
- Supports incorporating IVI classes
- Addresses backward compatibility with legacy instruments
- Addresses “forward” looking compatibility
- Address RF, Low Frequency, and Digital instrumentation
Standardized Software Architecture
(Preliminary Project Plan)

**PHASE 1**
Requirements Analysis
- Establish Group
- Initial Telecon
- Task Assignments
- Establish Schedule
- Establish "Approval" Criteria
- Establish Data Sharing "Method"
- Bi-Weekly Telecons
- Reqts Documentation

**PHASE 2**
Spec Outline
- Review Cycle
- Spec Outline
- Bi-Weekly Telecons
- Specification Outline
- Reqts Documentation
- Summary Review

**PHASE 3**
Trade Studies
- Peer Reviews
- Generate Spec
- Peer Reviews
- Task Assignments
- Bi-Weekly Telecons
- Data Consolidation
- Peer Reviews
- Specification Adjustment
- Summary Review

**PHASE 4**
Spec Develop
- Atc Review
- Approve & Release
- Finalize Spec
- Atc Review
- Approve and Release
- Finalize Draft Specification
- Peer Reviews
- Committee Review
- Approval and Release

**PHASE 5**
Atc Review

**TIME LINE - ?????**