



**Trusted Microelectronics Special Topic:  
Field Programmable Gate Array (FPGA)  
Assurance**

## Agenda

**February 27, 2018**

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|-------------------|--|
| 8:00am – 8:15am   | Welcome to MITRE (Bob Martin or Roberto Landau)  |
| 8:15am – 8:30am   | Welcome – Robert Gold, Director Enterprise Engineering, DASD(SE)   |
| 8:30am – 8:45am   | Workshop Charter and Organization – Gabe Mounce, USAF/AFRL   |
| 8:45am – 9:00am   | Highlights of the NDIA FPGA Assurance Workshop of March 1-2, 2017 – Brian Cohen, IDA   |
| 9:00am – 9:30am   | FPGA in the context of Supply Chain Risk Management – Jeff Krieg, NSA  |
| 9:30am – 10:00am  | JFAC FPGA Assurance Risk and Mitigation Subgroup – Mike Johnson, SNL   |
| 10:00am – 10:15am | break  |
| 10:15am – 10:45am | JFAC FPGA Assurance Collaboration Subgroup - Gabe Mounce, AFRL   |
| 10:45am – 12:00pm | JFAC FPGA Assurance Strategy, Policy and Guidance Subgroup<br>DOD FPGA Assurance Strategy – Ray Shanahan, DASD(SE)<br>Discussion |
| 12:00pm – 1:00pm  | Lunch on your own (MITRE Cafeteria available)  |
| 1:00pm – 1:45pm   | Review of Trusted and Assured Microelectronics Program FPGA Assurance Activities – Jeremy Muldavin, DASD(SE)<br>Discussion       |
| 1:45pm – 2:00pm   | Recent Title III and SBIR Activities – Gabe Mounce, USAF/AFRL  |
| 2:00pm – 2:15pm   | FPGA Sources – Aman Gahoonia, DMEA   |
| 2:15pm – 2:45pm   | FPGA TORs – Vik Rao, Aerospace   |
| 2:45pm – 3:00pm   | Break  |
| 3:00pm – 4:00pm   | FPGA Vendors Panel<br>Xilinx, Intel PSG, MicroSemi, Others?  |
| 4:00pm – 4:30pm   | eFPGA Ecosystem (Vendors – Implementors)   |
| 4:30pm – 5:00pm   | Discussion and Wrap-up for the Day   |



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**February 28, 2018**

- 8:00am – 8:15am Welcome to MITRE (Bob Martin or Roberto Landau)
- 8:15am – 9:00am Future Trends, FPGAs, SOCs and Reconfigurable Computing – Speaker?
- 9:00am – 10:00am System Integrator Perspective (Primes, Electronics System Designer and Integrators)
- Discussion
- 10:00am – 11:00am Firmware Assurance – Moderator Tom Hurt, DASD(SE)
- Joe Jarzombek, Synopsis
- John Hallman, MacB
- Jason Oberg, Tortuga
- Other TBD
- 11:00am – 12:00pm Wrap-up Discussion
- Feedback on Strategy
- Gaps
- 12:00pm – 1:00pm Lunch on your own (MITRE Cafeteria available)
- Convene Classified Session – Room is cleared**
- 1:00pm – 2:00pm Trust in FPGA Deep Dive – Mike Johnson, Sandia
- 2:00pm – 3:00pm FPGA Assurance Strategy (secret) – Ray Shanahan, DASD(SE)
- Discussion
- 3:00pm – 4:00pm Discussion of Strategy – Ray Shanahan
- 4:00pm – 4:30pm Discussion Next Steps
- 4:30pm – 5:00pm Summary and Wrap-up for the Day

Agenda subject to change without notice.