

NDIA



TRUSTED MICROELECTRONICS WORKSHOP

Onsite Agenda

February 2, 2017

Lockheed Martin Global Vision Center, Arlington, VA

7:30am – 8:30am

REGISTRATION AND NETWORKING COFFEE

8:30am – 8:35am

WELCOME

Mr. James Chew, Group Director, National Security Programs, Cadence Design Systems

8:35am – 8:45am

WORKSHOP INTRODUCTION

Dr. Jeremy Muldavin, Deputy Director, Defense Software & Microelectronics Assurance Activities, ODASD (SE)

8:45am – 9:45am

TRUST AND INTEGRITY PERSPECTIVE: 3RD PARTY INTELLECTUAL PROPERTY IN DEFENSE SYSTEMS

Moderator: Dr. Brian Cohen, Research Staff Member, Institute for Defense Analyses

- Dr. Michael Bear, Technical Director - Systems Engineering, BAE Systems
- Mr. Claude Goldsmith, Principal Engineer, Lockheed Martin Corporation
- Dr. William Phillips, Director, Advanced Technology, Northrop Grumman Corporation
- Dr. Warren Snapp, Manager, Solid-State Electronics Technology, The Boeing Company
- Dr. Thomas Kazior, Senior Principal Fellow, Advanced Microelectronics, Raytheon Company

9:45am – 10:00am

NETWORKING COFFEE BREAK

10:00am – 11:00am

INTELLECTUAL PROPERTY PORTFOLIOS: ELECTRONIC DESIGN AUTOMATION (EDA) PROVIDERS

Moderator: Mr. John Hallman, Program Manager, MacAulay-Brown, Inc.

- Mr. Dino Bekis, VP Product Marketing, IP Group at Cadence, Cadence
- Mr. Serge Leef, VP New Ventures, Mentor Graphics
- Dr. Chi-Foon Chan, President and co-CEO, Synopsys

11:00am – 11:30am

Q&A WITH PANEL

11:30am – 12:30pm

LUNCH (provided)

12:30pm – 12:45pm

TRUSTED MICROELECTRONICS JOINT WORKING GROUP

Introduction and Moderated by: Dr. Dan Radack, Assistant Director, Institute for Defense Analyses

12:45pm – 1:45pm

TM JWGT TEAM 1: FUTURE REQUIREMENTS

Lead: Mr. Charley Adams, Director, Programs, Northrop Grumman Corporation

- Dr. Scott Anderson, Research Scientist, Sr. Staff, Lockheed Martin Corporation
- Dr. Mike Fritze, Senior Fellow, Potomac Institute for Policy Studies
- Mr. Craig Herndon, Director, Critical Technologies Innovation

Center, Naval Surface Warfare Center-Crane Division

- Mr. Manny Trejo, Mission Lead, DoD

1:45pm – 2:45pm

TM JWG TEAM 2: TRUSTABLE LEADING EDGE
TECHNOLOGY ACCESS

Lead: Mr. Ezra Hall, Executive, GLOBALFOUNDRIES U.S. 2

- Ms. Anita Balachandra, Senior Vice President, TechVision21
- Mr. James Chew, Group Director, National Security Programs, Cadence Design Systems
- Dr. Brian Cohen, Research Staff Member, Institute for Defense Analyses
- Mr. Vashisht Sharma, Research Staff Member, Institute for Defense Analyses
- Dr. Paul Syers, Research Fellow, Potomac Institute for Policy Studies
- Ms. Kelly Henning, Intellectual Asset Management Lead, Northrop Grumman

2:45pm – 3:00pm

NETWORKING COFFEE BREAK

3:00pm – 4:00pm

TM JWG TEAM 3: TRUSTABLE MICROELECTRONICS
STANDARD PRODUCTS

Lead: Mr. Ken Lebo, Systems Engineer, JACOBS CSG

- Mr. Dean Brenner, Product Line Director, Honeywell Aerospace
- Mr. John Hallman, Program Manager, MacAulay-Brown, Inc.
- Mr. Neal Levine, Defense Microelectronics Activity

4:00pm – 5:00pm

TM JWG TEAM 4: NEW METHODS TO INSTILL TRUST IN
SEMICONDUCTOR FABRICATION

Lead: Dr. Pat Hays, Chief Engineer, The Boeing Company

- Dr. Todd Bauer, Staff Member, Sandia National Laboratories
- Dr. Greg Creech, Founder, GLC Consulting, LLC
- Mr. Steve McNeil, Principal Engineer, Xilinx
- Mr. John Monk, Director, Semiconductor Foundry Operations, Northrop Grumman Corporation

5:00pm – 5:15pm

WRAP-UP

Dr. Brian Cohen, Research Staff Member, Institute for Defense Analyses

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